

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re the Application of:

TI-30112

Tai H. Nguyen, et al.

Serial No:

Filed: November 8, 2001

For: Shared Component Clock Protection for Multicore DSP Device

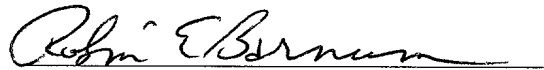
PRELIMINARY AMENDMENT

Ass't Commissioner for Patents

Washington, DC 20231

Dear Sir:

EXPRESS MAILING" Mailing Label No. EL645453285. Date of Deposit November 8, 2001. I hereby certify that this paper is being deposited with the U.S. Postal Service Express Mail Post Office to Addressee Service under 37 CFR 1.10 on the date shown above and is addressed to: Ass't Commissioner for Patents, Washington, D.C. 20231.

  
Robin E. Barnum

Please amend the specification by inserting before the first line, the following sentence:

--This application claims priority under 35 USC §119(e)(1) of Provisional Application Number 60/248,905, filed November 15, 2000.--

Respectfully submitted,



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